

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Frederick E. Cooperrider, Reg. No. 36,769 on 09/24/2008.

The application has been amended as follows:

Claim 5. (Canceled)

Claim 11. (Canceled)

2. Claims 1, 3-4, 6-7, 9-10 and 12 are allowed.
3. Claims 2, 5, 8, 11 and 13-20 are canceled.
4. The following is an examiner's statement of reasons for allowance:

The prior art of records fails to disclose a method and an apparatus for improving at least one of speed and efficiency when executing a level 3 dense linear algebra processing on a computer comprising: automatically setting an optimal machine state on the computer for said processing by selecting an optimal matrix subroutine from among a plurality of matrix subroutines for performing a level 3 matrix multiplication and further determining a size of each of matrices involved in said matrix multiplication; and

selecting one of said matrices to reside in an L1 cache, based on said determined size wherein said selecting a matrix subroutine comprises determining which of said matrix subroutines is consistent with said matrix selected to reside in said L1 cache along with other features as cited in independent claims 1 and 7.

The closest found prior art is Fred et al. ("Superscalar GEMM-based Level 3 BLAS - The On-going Evolution of a Portable and High-Performance Library"). Fred et al. discloses a method for improving efficiency when executing a level 3 dense linear algebra processing on a computer comprising: setting an optimal machine state on the computer for said processing by selecting an optimal matrix subroutine from among a plurality of matrix subroutines. However, Fred et al. fail to clearly address or teach the step of determining a size of each of matrices involved in said matrix multiplication; and selecting one of said matrices to reside in an L1 cache, based on said determined size wherein said selecting a matrix subroutine comprises determining which of said matrix subroutines is consistent with said matrix selected to reside in said L1 cache as seen in the independent claims above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to CHAT C. DO whose telephone number is (571)272-3721. The examiner can normally be reached on Tue-Fri 7:00AM to 5:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lewis Bullock can be reached on (571) 272-3759. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Chat C. Do/  
Primary Examiner, Art Unit 2193

September 24, 2008